

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:	TI-37611
Sanish Koshi Jacob	Art Unit: 2815
Serial No: 10/710,692	Examiner: Michael B. Shingleton
Filed: July 28, 2004	Conf. No.: 4691
For:	High Voltage Circuits Implemented Using Low Voltage Transistors

ELECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This election is offered in response to the Examiner's restriction requirement mailed March 9, 2007.

Applicant hereby elects to pursue Group 1, Claims 1-6 and 11-13.

Respectfully submitted,

/Alan K. Stewart/
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